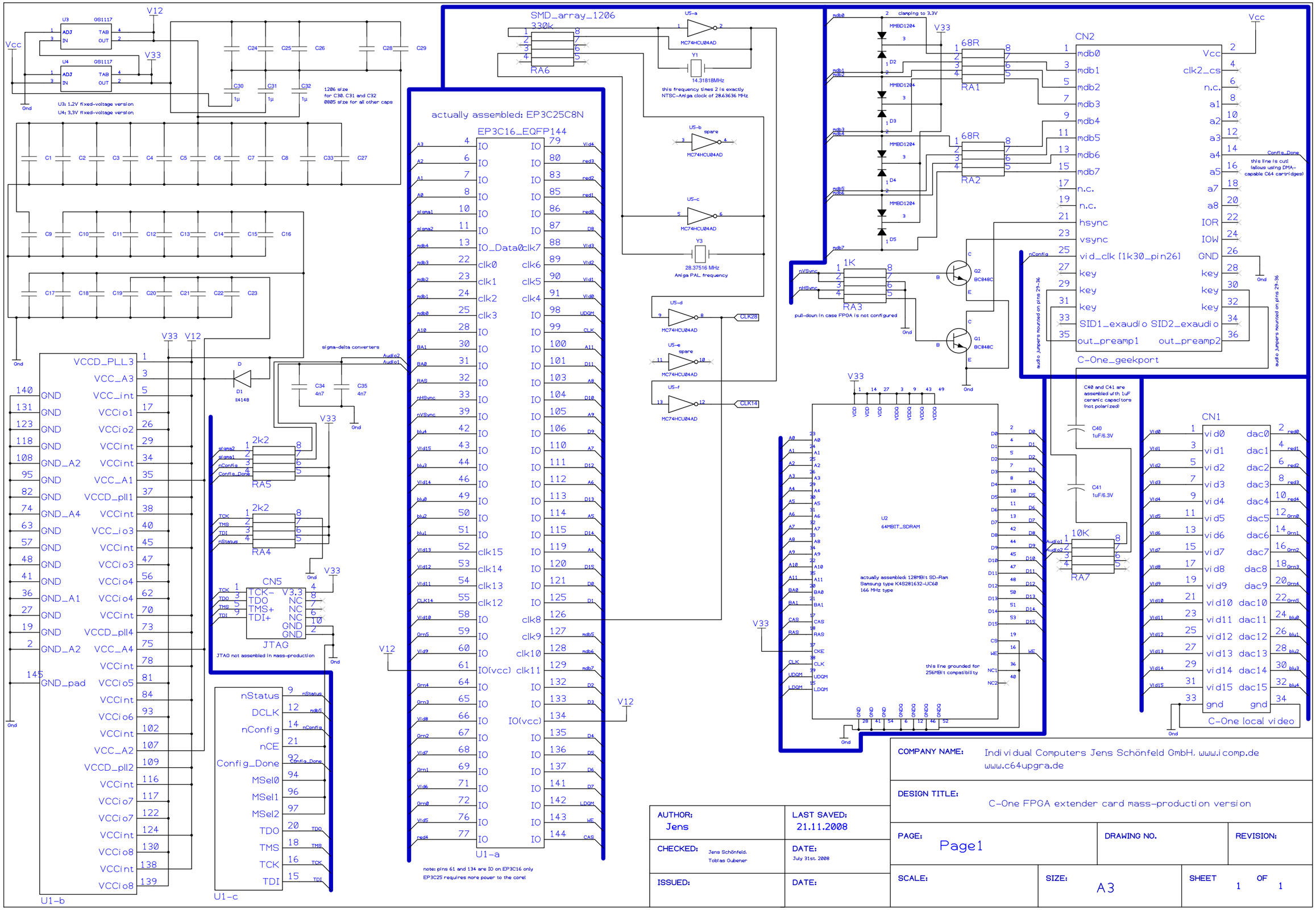
U3: 1.2V fixed-voltage version
U4: 3.3V fixed-voltage version
1206 size for C30, C31 and C32
0805 size for all other caps
actually assembled: EP3C25C8N
EP3C16_EQFP144
SMD_array_1206 330k
RA6
14.31818MHz
this frequency times 2 is exactly NTSC-Amiga clock of 28.63636 MHz
28.37516 MHz
Amiga PAL frequency
clamping to 3.3V
68R
RA1
RA2
1K
RA3
pull-down in case FPGA is not configured
CN2
C-One_geekport
this line is cut (allows using DMA-capable C64 cartridges)
audio jumpers mounted on pins 29-36
C40 and C41 are assembled with 1uF ceramic capacitors (not polarized)
10K
RA7
CN1
C-One local video
sigma-delta converters
2k2
RA5
RA4
CN5
JTAG
JTAG not assembled in mass-production
U1-b
U1-c
U1-a
U2
64MBIT_SDRAM
actually assembled: 128MBit SD-Ram Samsung type K4S281632-UC60 166 MHz type
this line grounded for 256MBit compatibility
note: pins 61 and 134 are IO on EP3C16 only
EP3C25 requires more power to the core!
COMPANY NAME: Individual Computers Jens Schönfeld GmbH, www.icomp.de www.c64upgra.de
DESIGN TITLE: C-One FPGA extender card mass-production version
AUTHOR: Jens
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SHEET 1 OF 1